

(12) UK Patent Application (19) GB (11) 2 344 461 (13) A

(43) Date of A Publication 07.06.2000

(21) Application No 9928531.4

(22) Date of Filing 02.12.1999

(30) Priority Data

(31) 9826517

(32) 02.12.1998

(33) GB

(71) Applicant(s)

Arima Optoelectronics Corporation
(Incorporated in Taiwan)
6th Floor, No 327, Sung Lung Road, Taipei, Taiwan

(72) Inventor(s)

Wang Nang Wang
Stephen Sen-Tien Lee

(74) Agent and/or Address for Service

Page Hargrave
Southgate, Whitefriars, Lewis Mead, BRISTOL,
BS1 2NT, United Kingdom

(51) INT CL⁷

H01L 33/00, H01S 5/323

(52) UK CL (Edition R)

H1K KEAA KELD K1EA K1EA1 K2R3A K2S1C K2S1D
K2S1E K2S16 K2S17 K9B1 K9B1A K9M1 K9N3 K9P3
K9S

(56) Documents Cited

GB 2338107 A GB 2317053 A US 5798537 A

(58) Field of Search

UK CL (Edition R) H1K KEAA KEAX KELD KELX
INT CL⁷ H01L
ON LINE, W.P.I., EPODOC, JAPIO

(54) Abstract Title

Semiconductor light emitting devices

(57) The device comprises a sapphire substrate 1 having a porous surface layer or porous structure, an InAlGa_N buffer layer 2, an n-type GaN layer 3, a Group III-V active layer 4, a p-type AlGa_N hole emitting layer 10, a p-type InGa_N contact layer 11 and a metallic ohmic contact. The device may be a laser or light emitting diode.

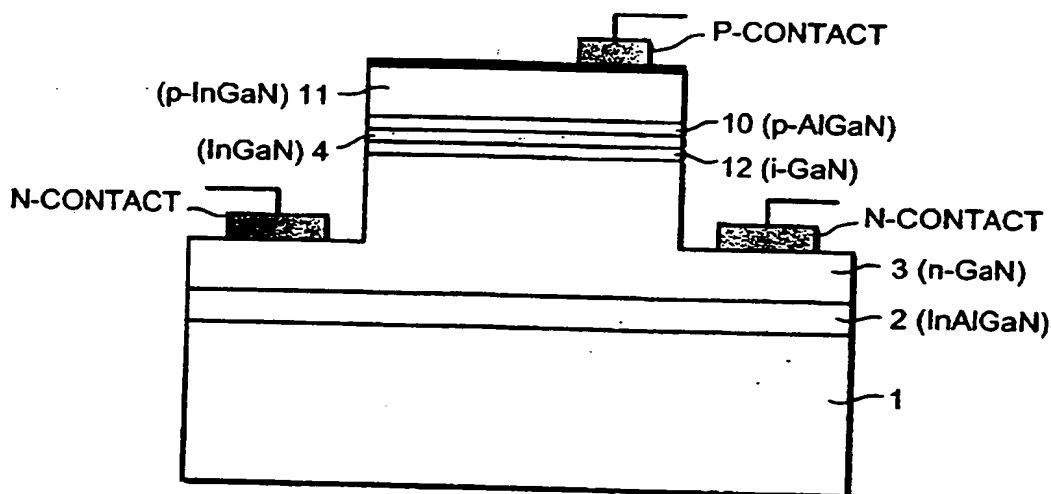


FIG. 3

GB 2 344 461 A

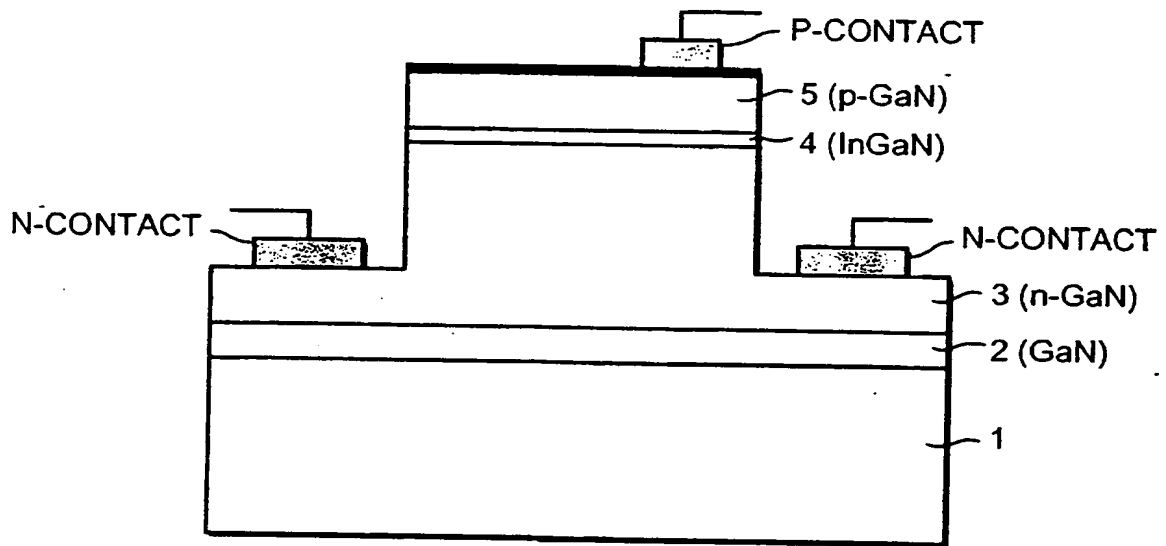


FIG. 1

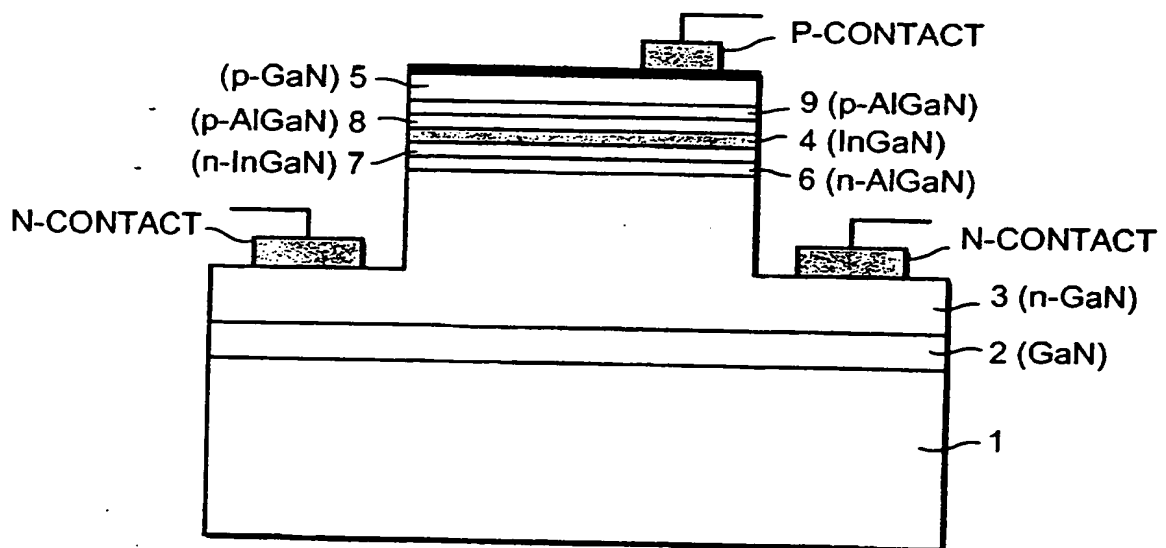


FIG. 2

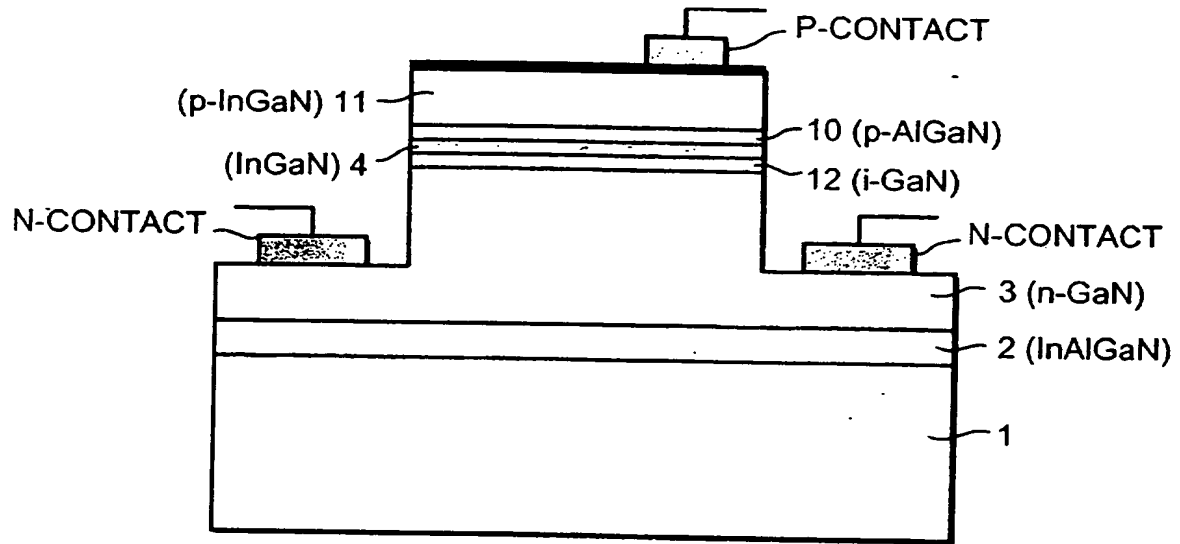


FIG. 3

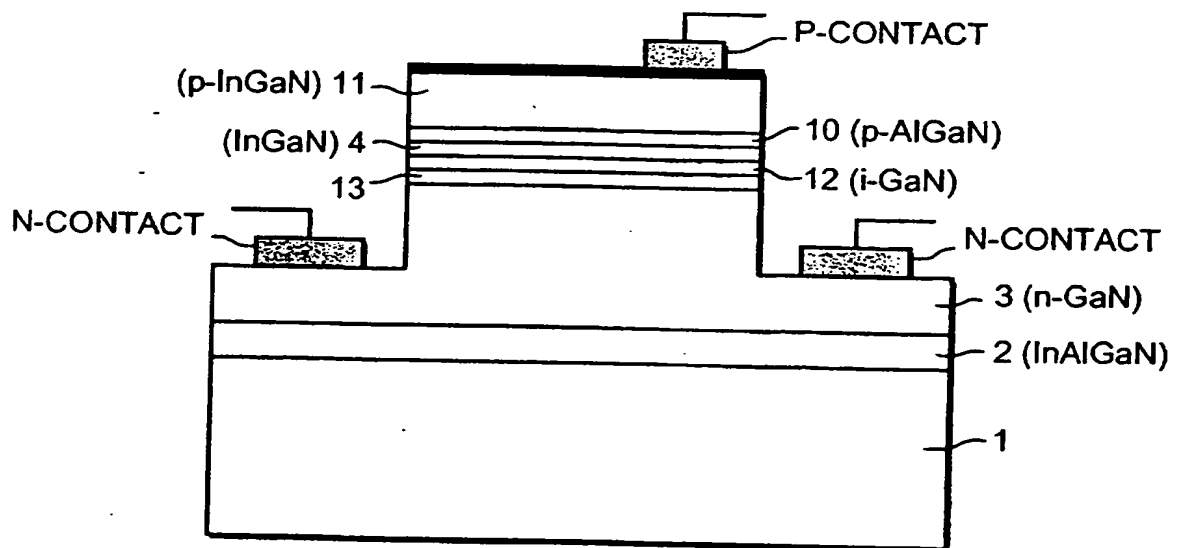


FIG. 4

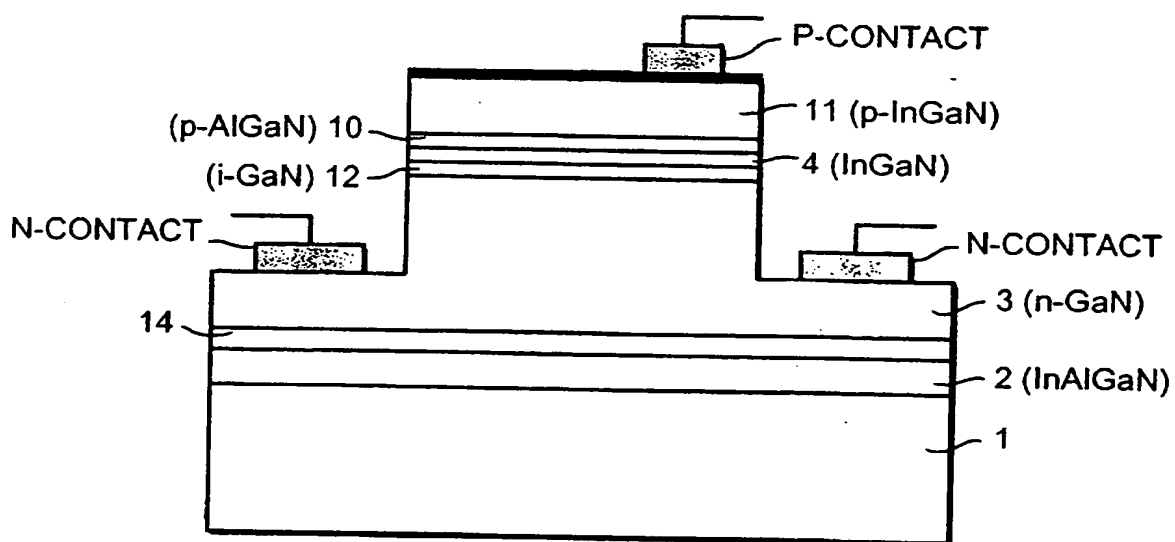


FIG. 5

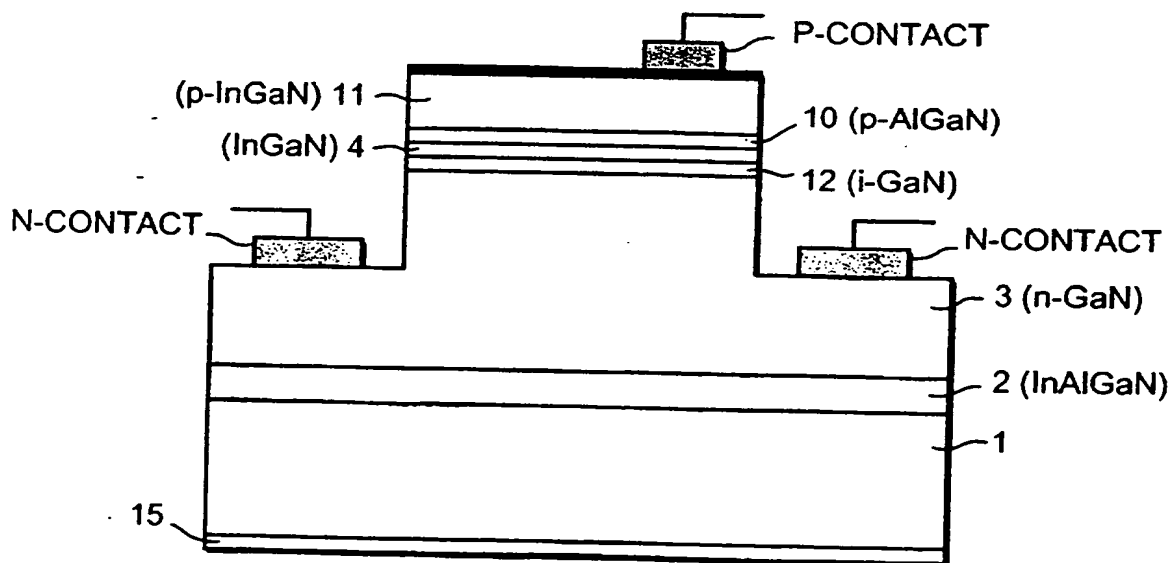


FIG. 6

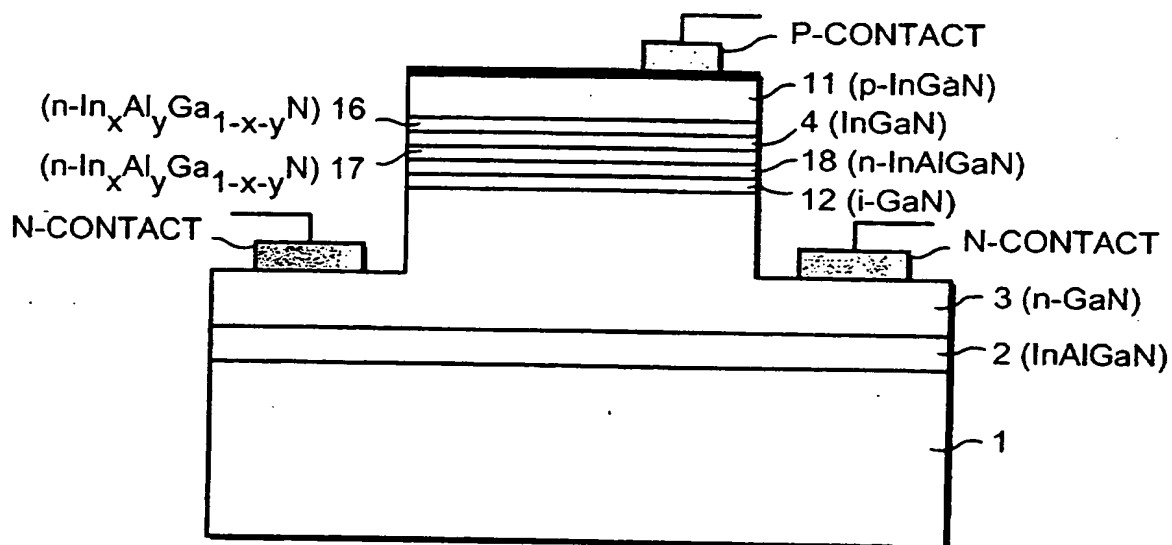


FIG. 7

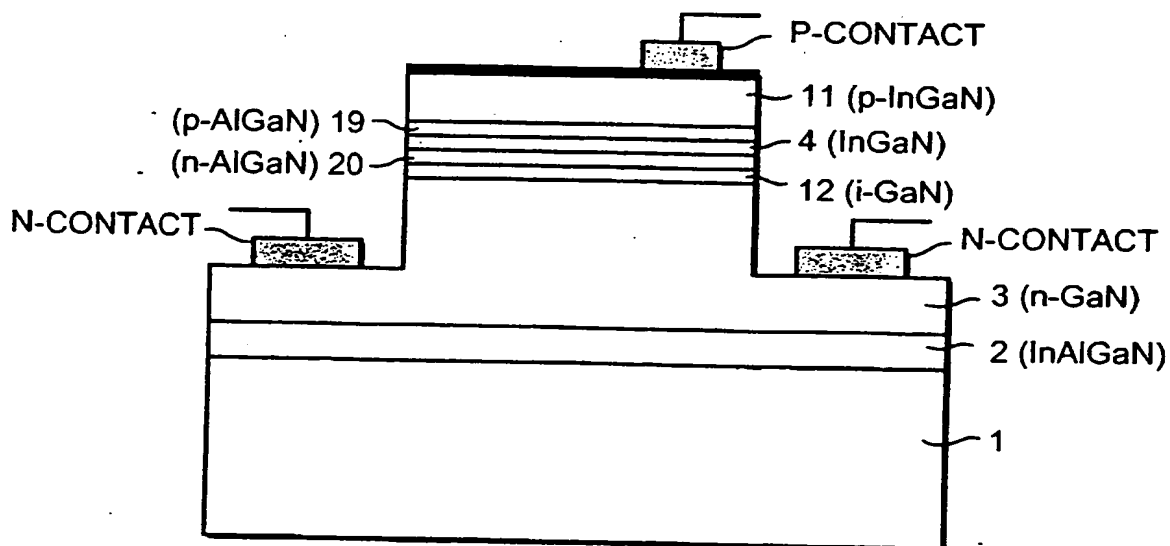


FIG. 8

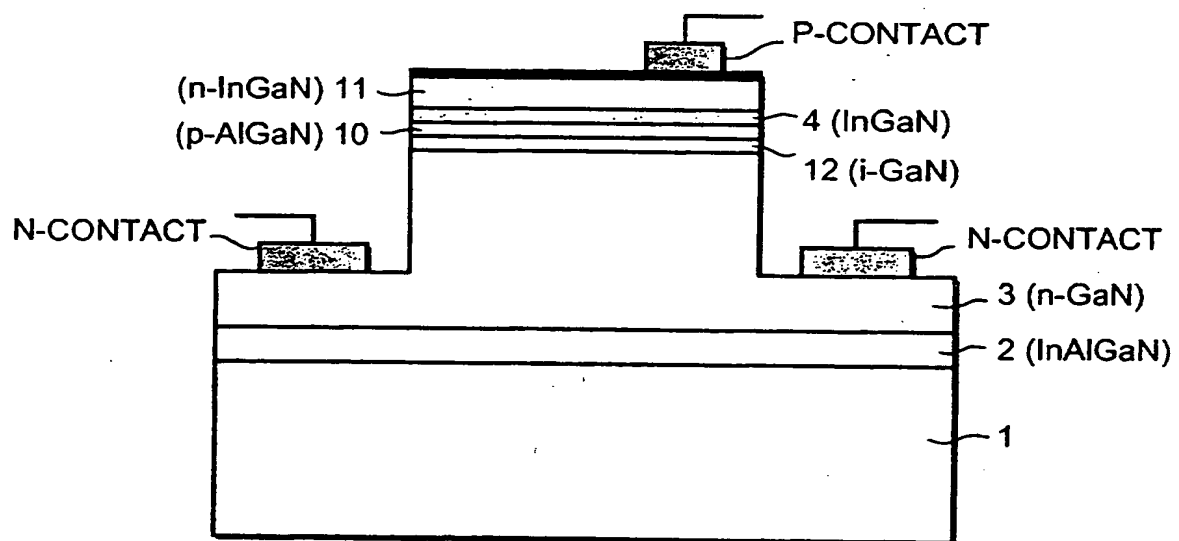


FIG. 9

SEMICONDUCTOR DEVICES

The present invention relates to semiconductor devices, more particularly light-emitting diodes (LEDs) and laser diodes (LDs).

5 According to the present invention, there is provided a light emitting diode structure with asymmetric claddings comprising:

10 a sapphire substrate with a thin porous surface layer or patterned porous structure to control the nucleation process of a quaternary InAlGa_N buffer layer and reduce strains;

a quaternary InAlGa_N buffer layer;

a n-GaN layer;

15 a tunnelling barrier and electron spreading layer made of III-V materials between said electron emitting layer and said active layer;

20 an active layer made of III-V materials and which creates single or multiple potential wells for electrons and holes where radiative recombination of electrons and holes occurs;

a hole emitting layer made of p-type AlGa_N;

a p-type contact layer made of p-type InGa_N; and

a metallic ohmic contact to the hole emitting layer.

Preferably, the tunnelling barrier and electron spreading layer has a thickness of 5 to 50 Å; the active layer has a width from 5 Å to 0.1 μm; the hole emitting layer has a width from 50 Å to 1 μm; and the p-type contact layer has a width from 50 Å to 1 μm. There may be an electron emitting layer (for example with a width from 50 Å to 1 μm) made of n-type or undoped III-V materials and which creates a potential well and electron accumulating layer for electrons.

10 In the design of an LED, there are the following to be achieved:

Good injection of holes in the active region.
Crystal-structure quality of the active region.
High internal efficiency.

15 Better light extraction.

To achieve the above, the following features are used in examples of the invention and are also features of the present invention, separately or in any combination.

20 Applying asymmetric claddings to prevent electron leakage from the active region.

Quaternary buffer layer.

Strained superlattices to stop threading dislocation penetration into the active region and to improve light extraction from the structure.

25 Quaternary alloy claddings to provide lattice-matching between claddings and the active region.

Laterally confined active region to prevent non-radiative recombination of carriers.

Porous sapphire surface to control nucleation process of a quaternary alloy buffer layer and to reduce strains inside the epitaxial layers.

5 Fig. 1 shows a simple conventional structure of a GaN-based LED, comprising a sapphire substrate 1; a GaN buffer 2; a n-GaN layer 3; an InGaN active layer 4; a p-GaN layer 5; an n-contact; and a p-contact.

10 Fig. 2 shows a prior art GaN-based single quantum well LED with symmetric claddings, comprising: a sapphire substrate; a GaN buffer 2; a n-GaN layer 3; a lower n-AlGaIn second cladding layer 6; a lower n-InGaIn first cladding layer 7; an InGaIn active layer 4; an upper p-AlGaIn first cladding layer 8; an upper p-AlGaIn second cladding layer 9; a p-GaN layer 5; an n-contact and a p-contact.

15 In this invention, asymmetric claddings to prevent electron leakage from the active region are employed to overcome problems related to poor p-type mobility.

20 In Fig. 3, an additional p-AlGaIn cladding layer 10 is added and contact p-GaN layer 5 of Fig. 1 is replaced by p-InGaIn layer 11. An additional i-GaN current spreading layer 12 is also added for better lateral uniformity of electron injection.

In Fig. 4 an additional electron emitting layer 13 is added for better lateral uniformity of electron injection.

25 In Fig. 5 an additional threading-dislocation-stopping layer 14 based on a strained superlattice provided either by an alternated superlattice structure or by a

distributed Bragg reflector layer which also acts as a mirror.

In Fig. 6 an external mirror 15 based on a metal layer or a metal-dielectric interference filter is used.

5 In Fig. 7 the strains in the active layer 4 are suppressed with tuning of quaternary alloy compositions in claddings 16 and 17 to achieve lattice-match. An additional graded cladding layer 18 serves the same purposes.

10 Fig. 8 shows an advanced structure which employs a lateral confinement of carriers inside the active region 4 which consists of separated InGaN islands embedded in the wider-gap matrix provided by confinement layers 19 and 20.

15 Fig. 9 shows an inverted version of the structure of Fig. 3 which makes it easier to prepare a front transparent metal contact.

In the above examples, crystals of a GaN-based compound semiconductor (3) are grown on the surface of buffer layer 2 represented by formula $(\text{Ga}_x\text{Al}_{1-x})_y\text{In}_{1-y}\text{N}$, where $0 \leq x \leq 1$, $0 \leq y \leq 1$ (Fig. 3). The incorporation of In suppresses the
20 occurrence of crystal defects and thus has excellent crystallinity and considerably superior flatness. The incorporation of In also enhances the growth rate of the crystal and thus reduces the deposition time required for the buffer layer. It is difficult to grow a p-type GaN
25 layer on conventional buffer layers because the film has extremely bad crystallinity. For this reason, conventionally, n-type GaN is grown on a sapphire substrate. According to an example of the present

invention, there is provided a growth method for p-type $(\text{Ga}_x\text{Al}_{1-x})_y\text{In}_{1-y}\text{N}$ to be directly grown on the buffer with an inverted LED configuration (Fig. 9).

5 The following are also features of the invention, separately or in any combination:

The use of a sapphire substrate 1 with thin porous surface layer or patterned structure to control the nucleation process of a GaN based buffer layer and reduce strains. This porous surface layer or patterned structure can be
10 produced by laser scanning, plasma etching or chemical vapour deposition growth with suitable masks.

The LED structures are asymmetrical. The main reason for this approach is that the mobility for p-type GaN (about few tens of $\text{cm}^2/\text{V}\cdot\text{sec}$) is much lower than n-type GaN ($300 \text{ cm}^2/\text{V}\cdot\text{sec}$). This is very different from other types of
15 compound semiconductor LED devices, which have compatible p- and n-type mobilities. Therefore extra layers introduced in the p-type GaN side prevent the leak of run away electrons (Figs. 3, 4, 5, 6 and 7).

20 The use of strained superlattices to stop threading dislocation penetration into the active region and improves light extraction from the structure (Fig. 5).

The use of quaternary alloy claddings to provide lattice-matching between claddings and the active region (Fig. 7).

25 The use of laterally confined active regions to prevent non-radiative recombination of carriers (Fig. 8). Such laterally confined active regions can be created using

quantum structures such as quantum dots with the composition $(\text{Ga}_x\text{Al}_{1-x})\text{In}_{1-y}\text{N}$.

CLAIMS

1. A light emitting diode structure with asymmetric claddings comprising:
 - 5 a sapphire substrate with a thin porous surface layer or patterned porous structure to control the nucleation process of a quaternary InAlGa_N buffer layer and reduce strains;
 - a quaternary InAlGa_N buffer layer;
 - a n-GaN layer;
 - 10 a tunnelling barrier and electron spreading layer made of III-V materials between said electron emitting layer and said active layer;
 - an active layer made of III-V materials and which creates single or multiple potential wells for electrons and holes where radiative recombination of
15 electrons and holes occurs;
 - a hole emitting layer made of p-type AlGa_N;
 - a p-type contact layer made of p-type InGa_N; and
 - a metallic ohmic contact to the hole emitting layer.
- 20 2. A structure according to claim 1, wherein the active layer has a thickness of 5 to 50 Å; the active layer has a width from 5 Å to 0.1 μm; the hole emitting

layer has a width from 50 Å to 1 μm; and the p-type contact layer has a width from 50 Å to 1 μm.

- 5 3. A structure according to claim 1, wherein, an electron emitting layer made of n-type or undoped III-V materials is added and which creates a potential well and electron accumulating layer for electrons.
- 10 4. A structure according to claim 1, wherein the porous surface layer or patterned structure is produced by laser scanning, plasma etching or chemical vapour deposition growth with suitable masks.
- 5 5. A structure according to claim 1 which is an asymmetrical structure.
- 15 6. A structure according to claim 1, using a strained superlattice to stop threading dislocation penetration into the active region and improve light extraction from the structure.
7. A structure according to claim 1, using a quaternary alloy cladding to provide lattice-matching between claddings and an active region.
- 20 8. A structure according to claim 1, using laterally confined active regions to prevent non-radiative recombination of carriers.



Application No: GB 9928531.4
Claims searched: All

Examiner: COLIN STONE
Date of search: 14 March 2000

Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.R): H1K(KEAA,KEAX,KELD,KELX)

Int Cl (Ed.7): H01L

Other: ON LINE,W.P.I.,EPODOC,JAPIO

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A,E	GB 2338107 A HEWLETT-PACKARD	
A	GB 2317053 A HEWLETT-PACKARD	
A	US 5798537 TOSHIBA	

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.